

SEMINAR ANNOUNCE

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AULA 103, S.NICCOLÒ BUILDING, Via ROMA 56, SIENA
15:00-16:00 FRIDAY 15th NOVEMBER 2019



TITLE:

ARM Neoverse N1 A new powerful System-on-Chip for Cloud-to-Edge Infrastructure

*ABSTRACT:

In the new highly connected world, the number of interconnected device is predicted to constantly grow at an unrestrainable pace.

These devices are crucially connected to the cloud or be driven by artificial intelligence algorithms at the edge the network and with additional real-time constraints. Arm's System-on-Chips (SoCs) are evolving for the creation of an adequate network and compute infrastructure to deliver the huge amount of data that is flowing between the cloud and the edge of the network. In particular, the cloud-to-edge computing needs high performance and secure systems and architecture. This talk will illustrate the high-level architecture and design of the Neoverse N1 platform. Some of the core and interconnect features, as well as some of the advancements will be illustrated. Such architecture is now available for Arm's first server and network systems.